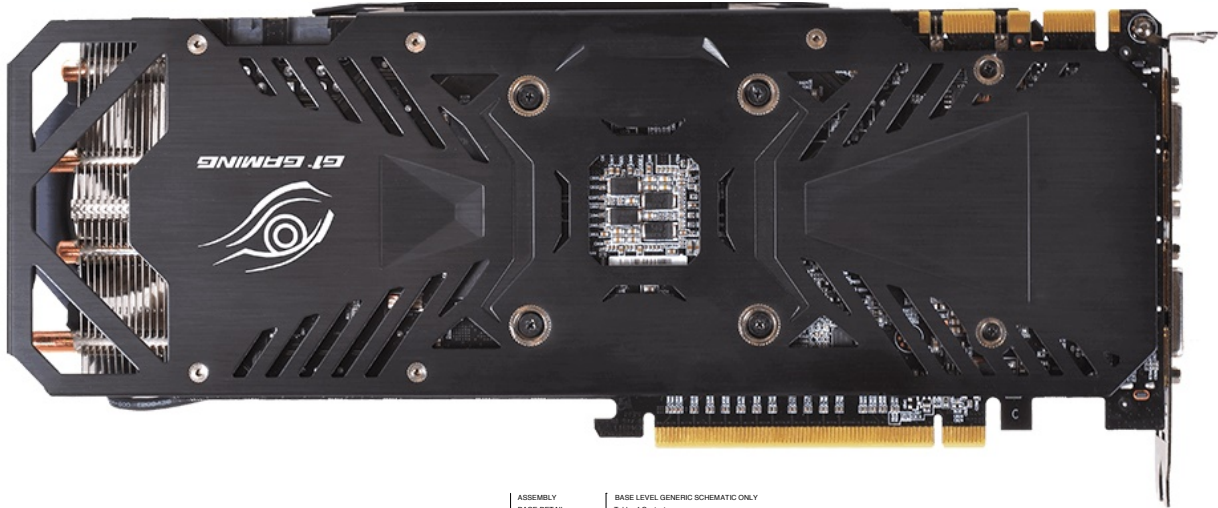


GV-N970G1 GAMING-4GD rev. 1.0



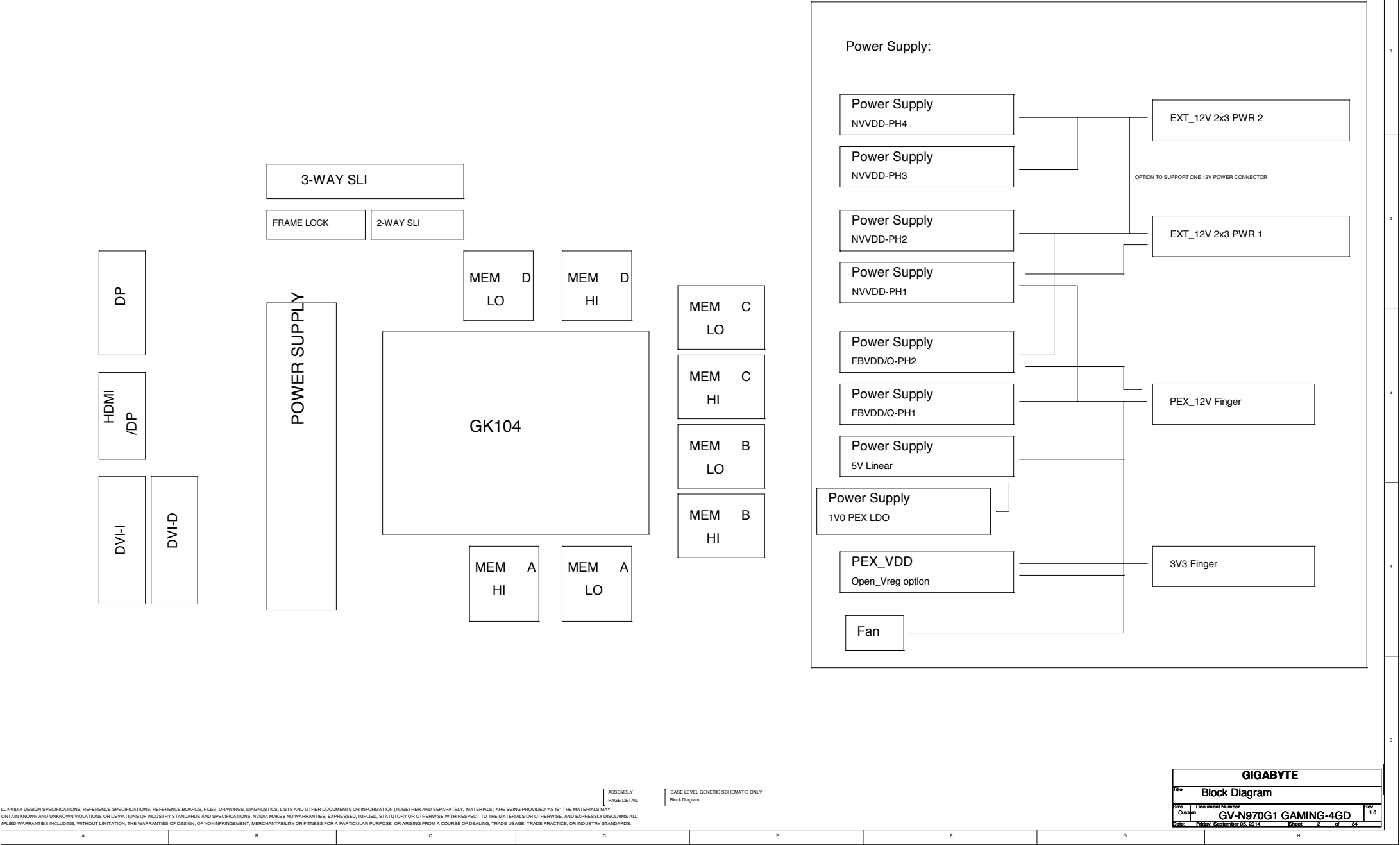
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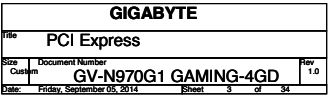
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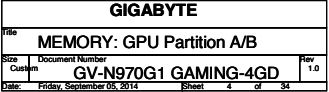


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Block Diagram





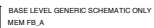
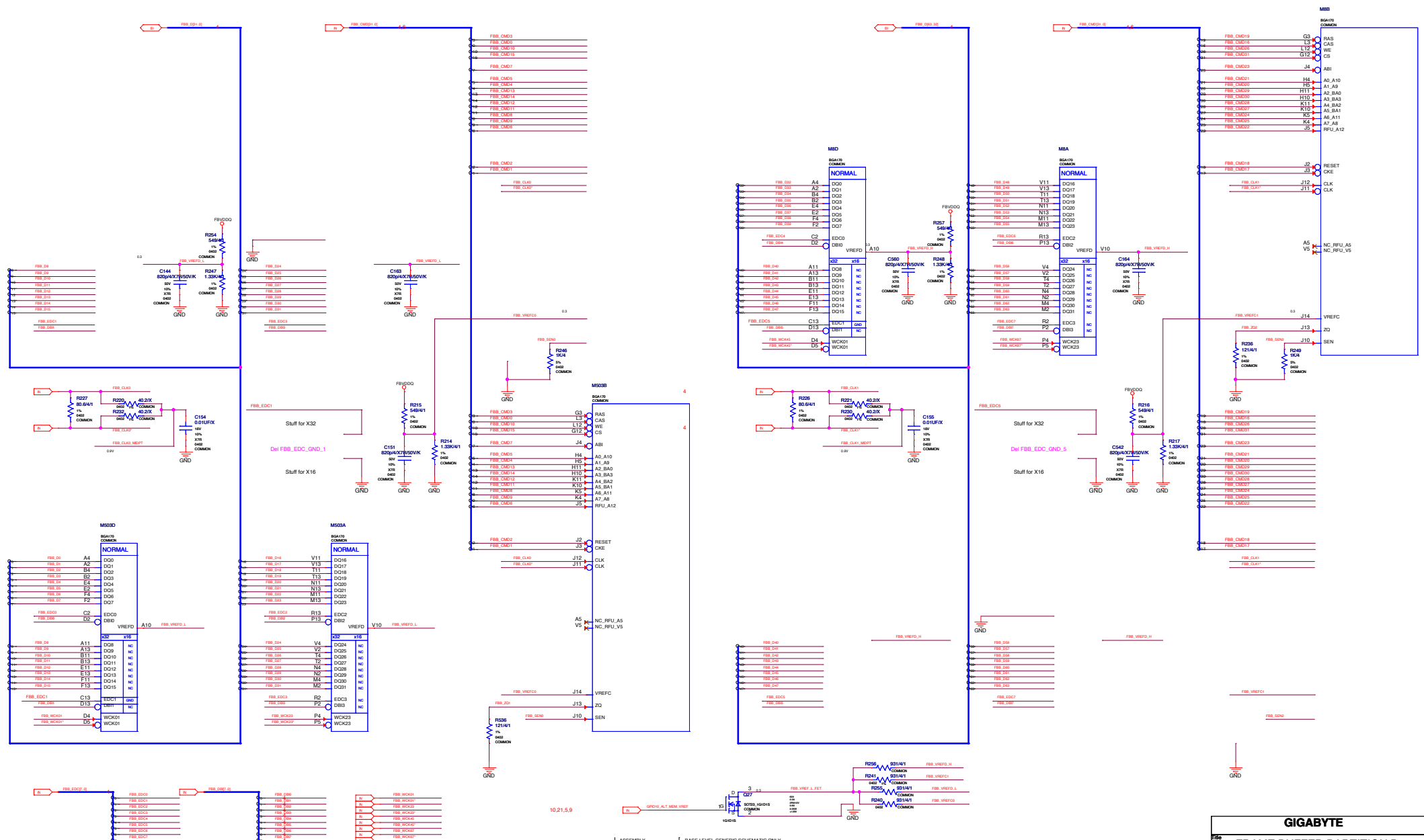


TABLE 1. FRAMEWORK FOR ATTENTION D



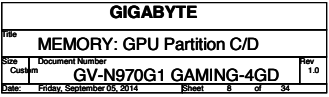
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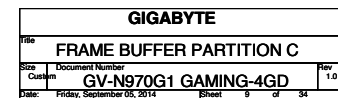
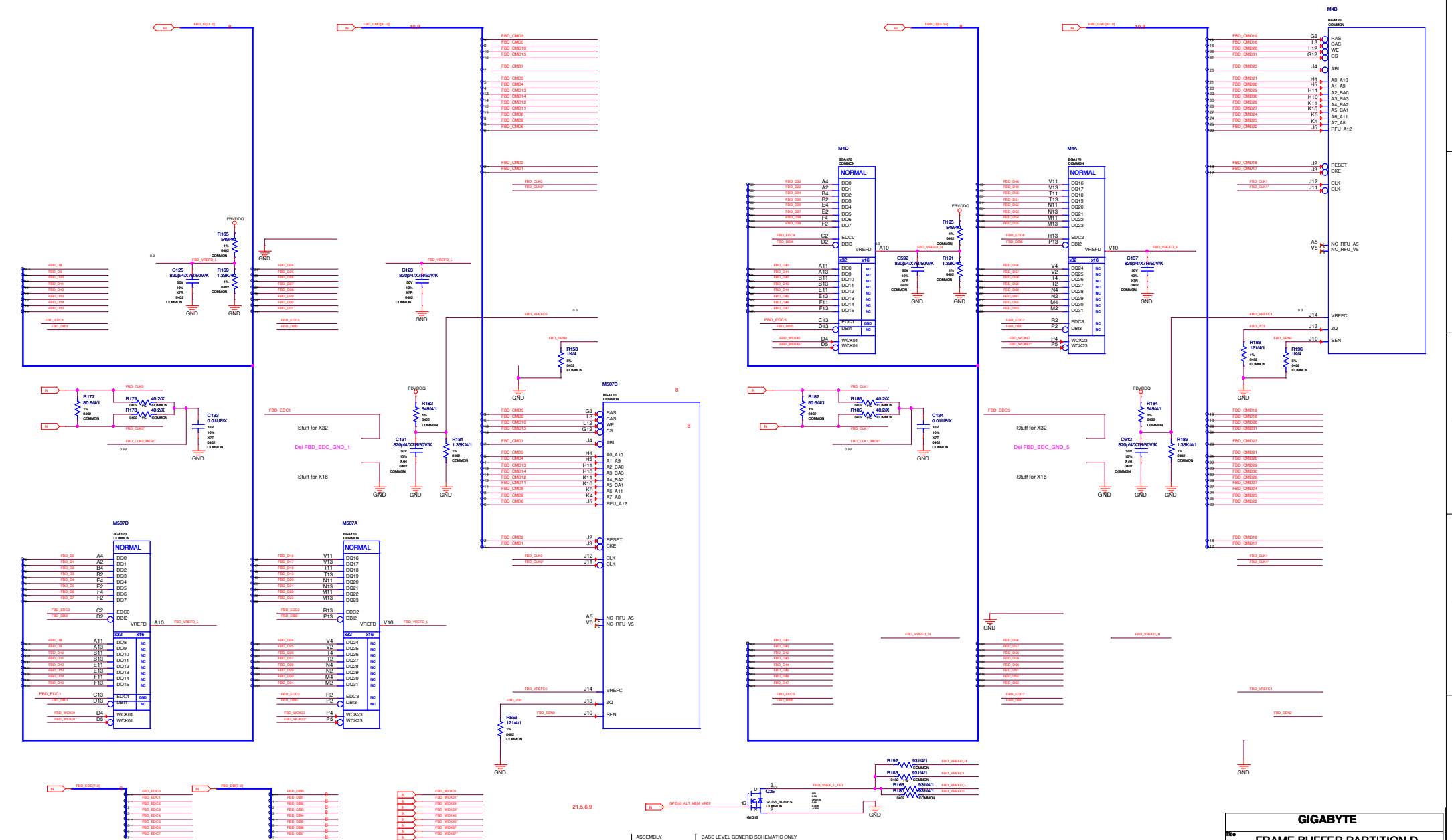


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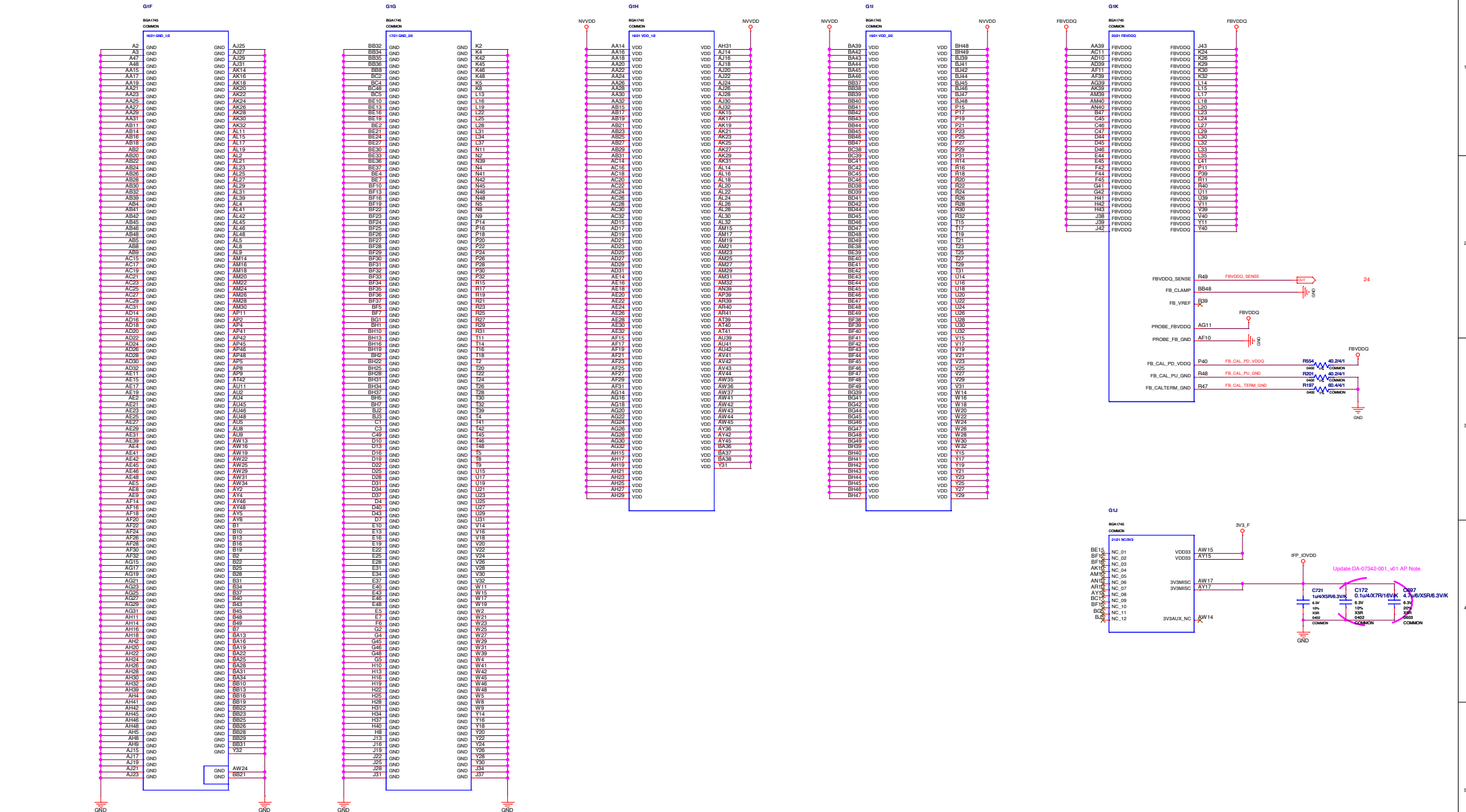


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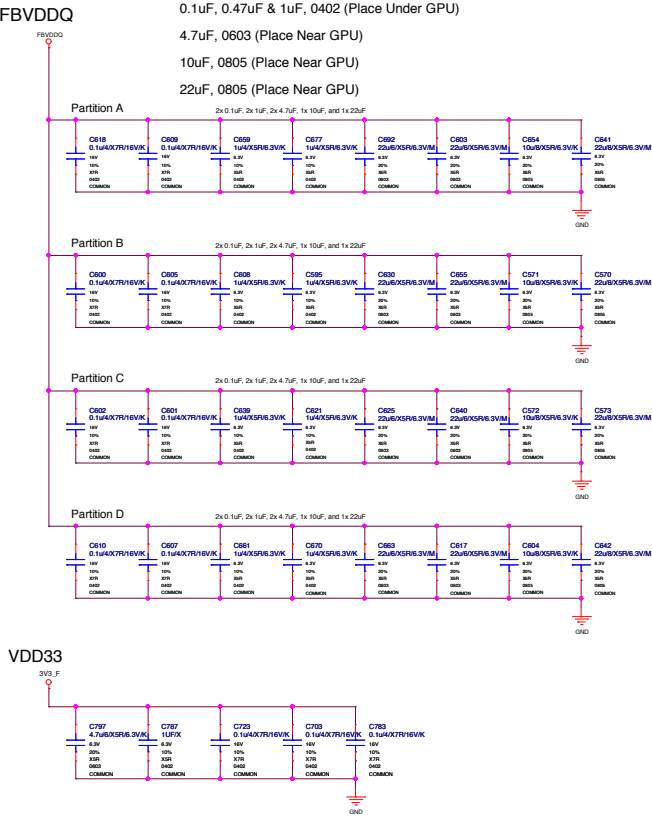
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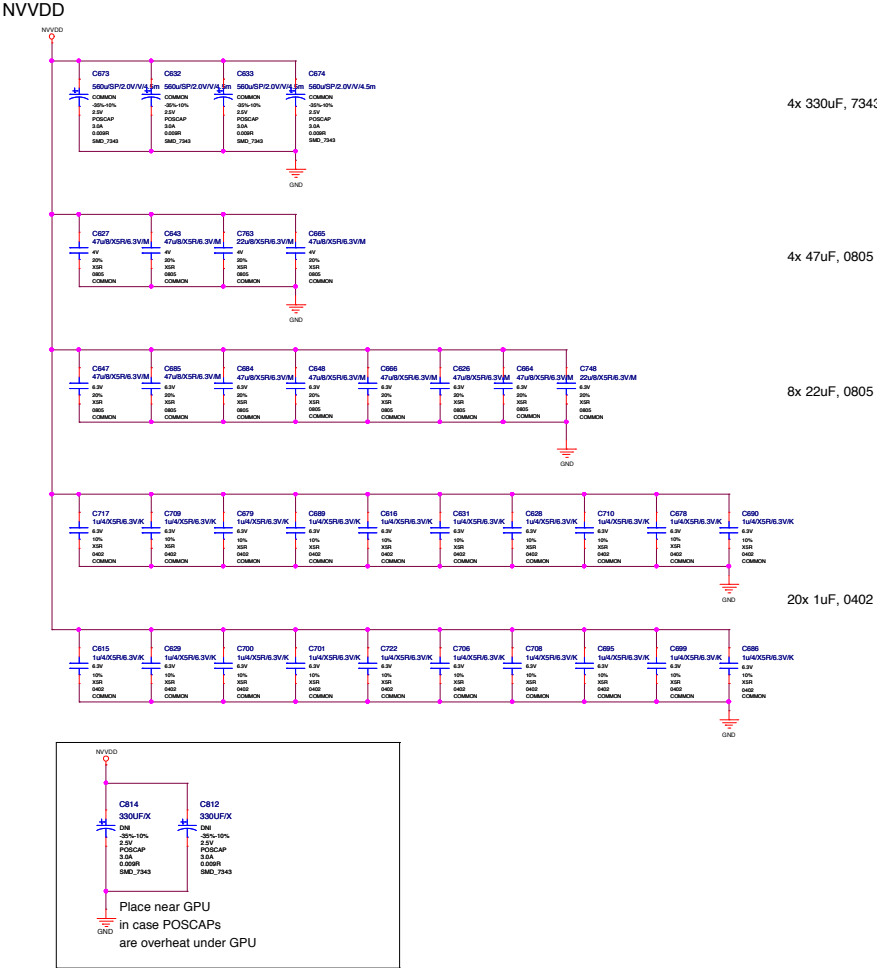
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GPU PWR and GND

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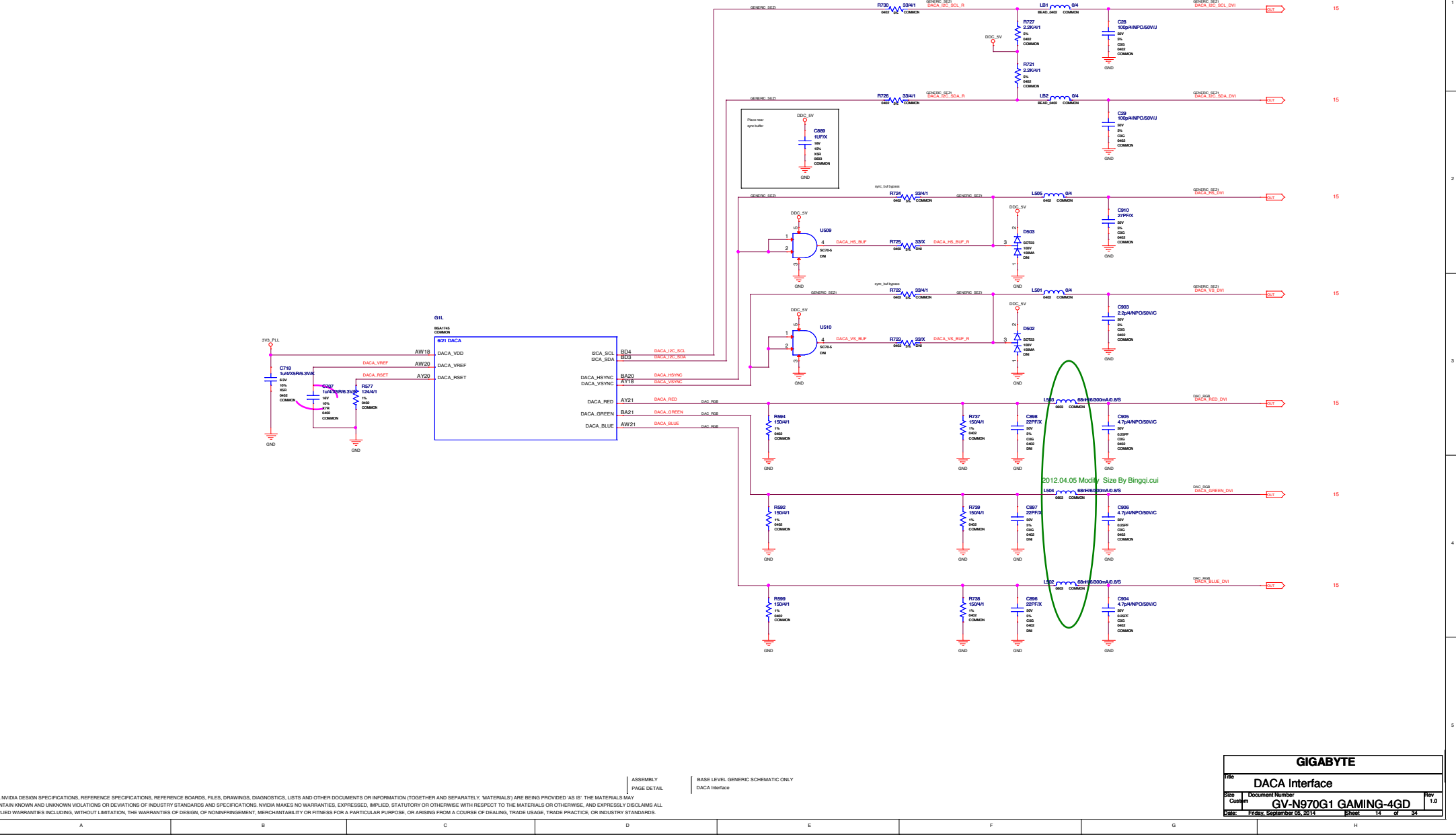
Based on GB2-X GDDR5 FBVDDQ Decap Guideline



NVVDD Decoupling caps. Place under GPU.



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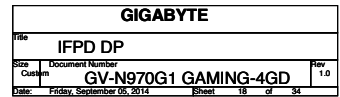
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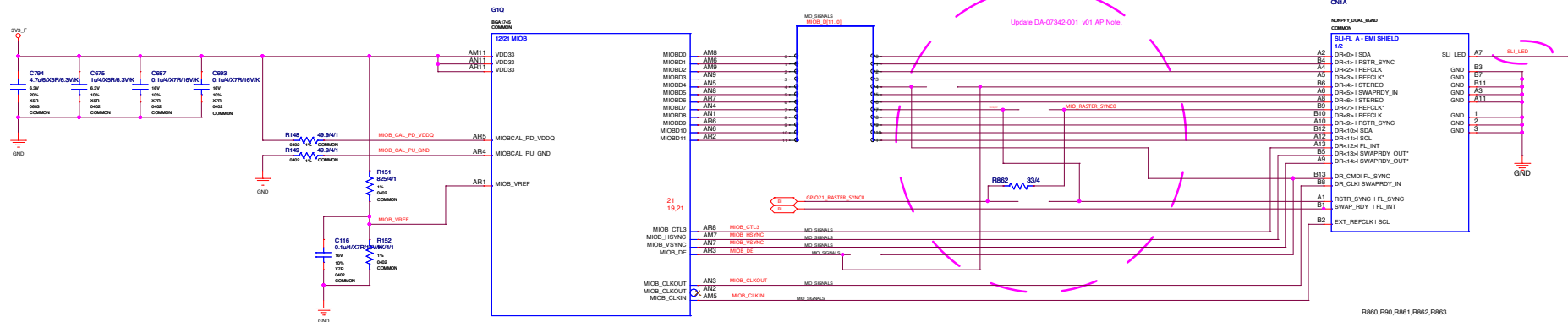
- * I2C to DDC level switching



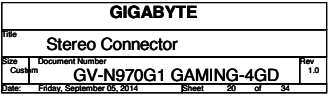
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2012.4.4 Modify By bingqi.cui 消耗22uH

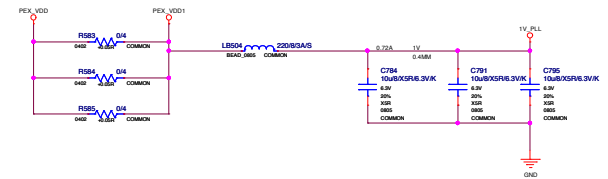
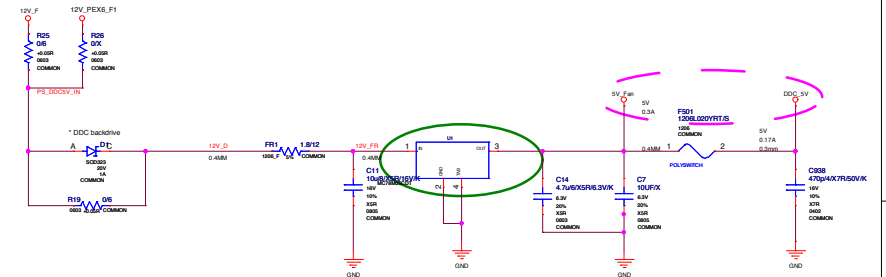
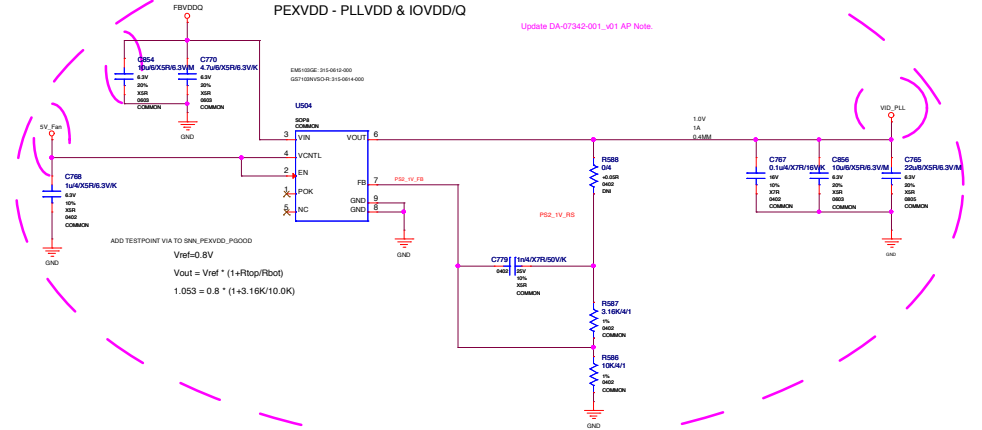
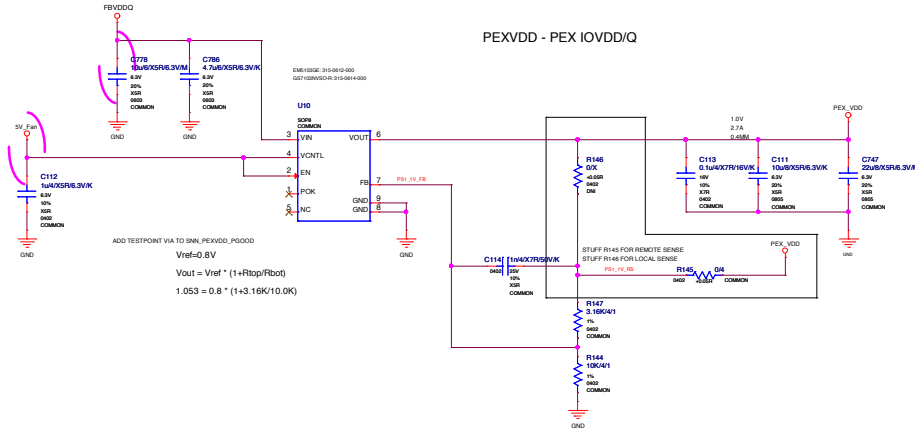
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Size	Document Number	Rev	
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Figure 1: Schematic diagram of the 1000BASE-T Ethernet PHY. The diagram shows a 4-pair differential signaling system. Each pair consists of a transmit (TX) and receive (RX) line. The TX lines are labeled F611, F608, F606, F614, F607, F605, F613, and F616. The RX lines are labeled F612, F609, F607, F615, F608, F606, F614, and F616. The TX lines are connected to a common ground, and the RX lines are connected to a common ground. The TX lines are also connected to a common ground through a 100Ω resistor. The RX lines are connected to a common ground through a 100Ω resistor. The TX lines are also connected to a common ground through a 100Ω resistor. The RX lines are connected to a common ground through a 100Ω resistor.

Update DA-07342-001_v01 AP Note

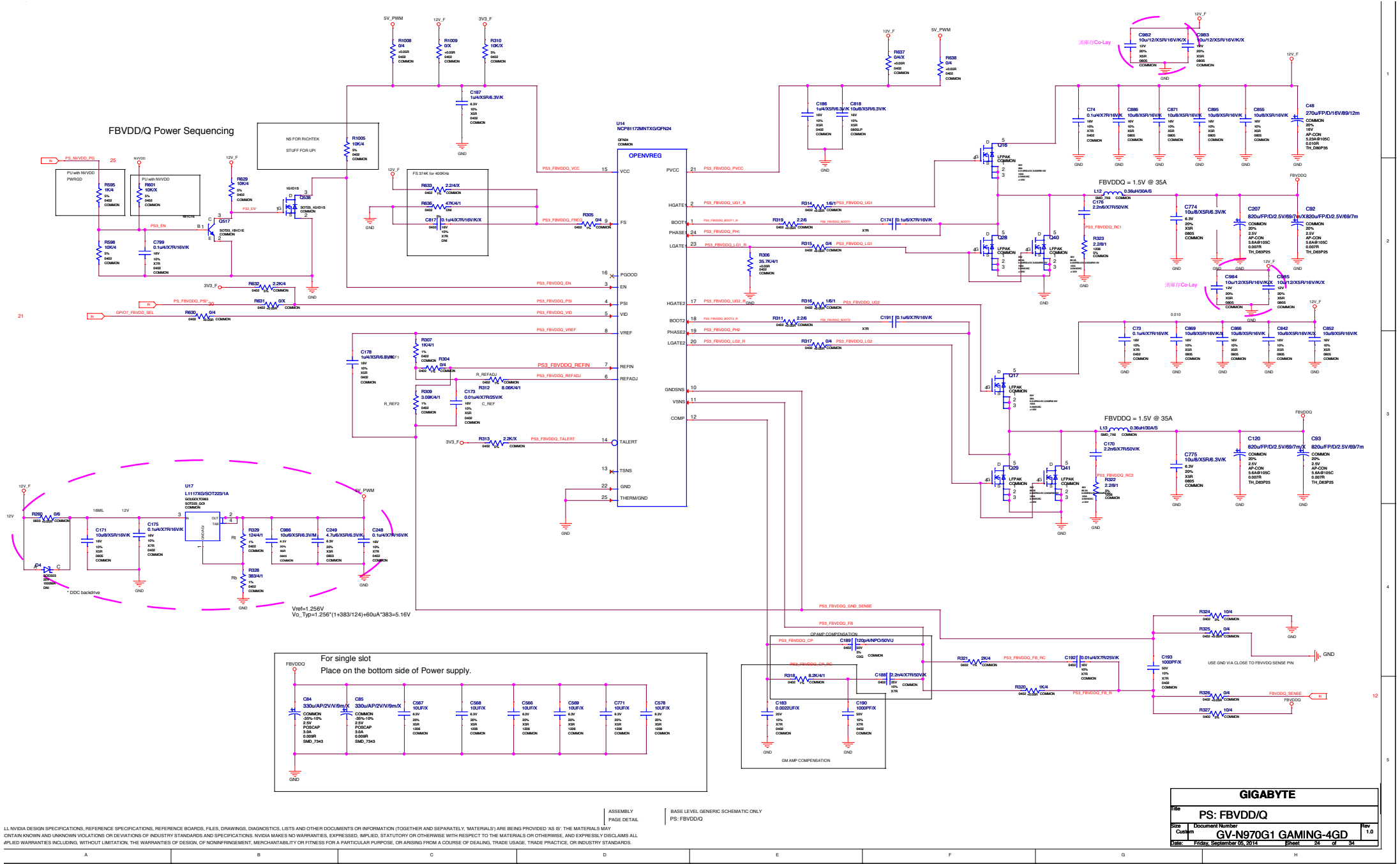
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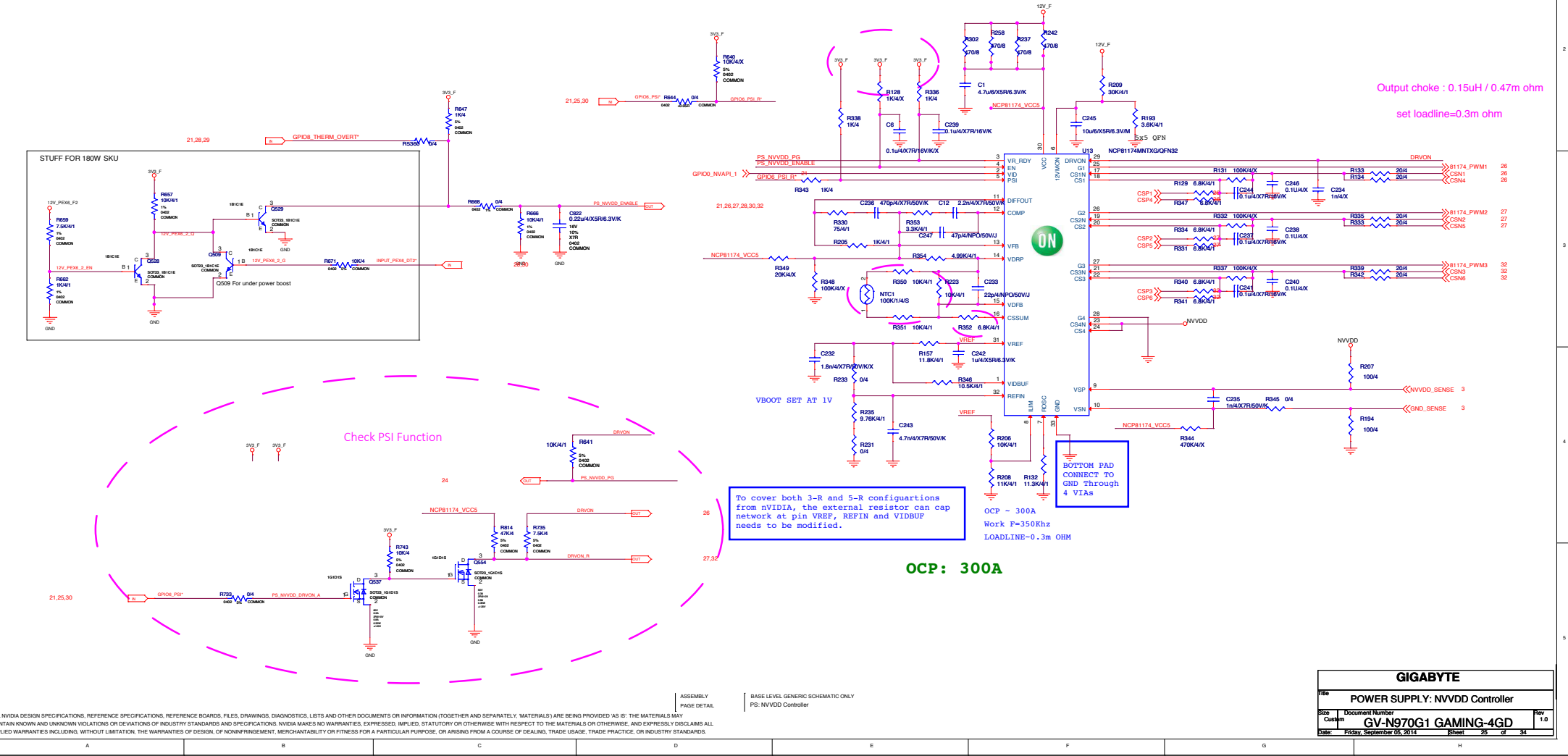
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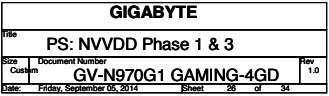
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PS: 5V, PEX_VDD

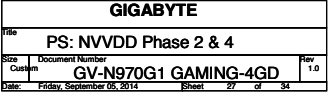
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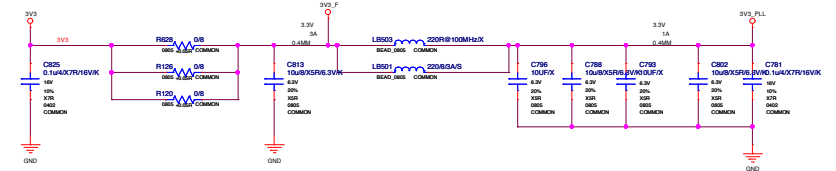
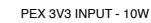
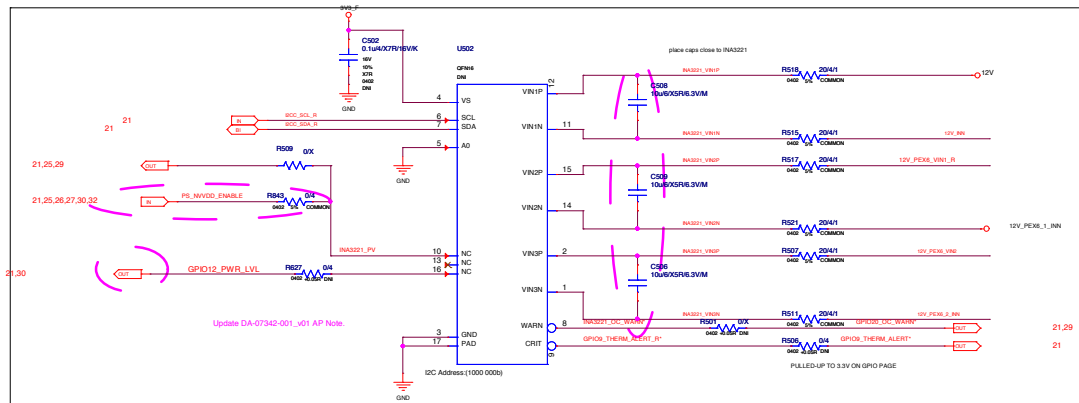
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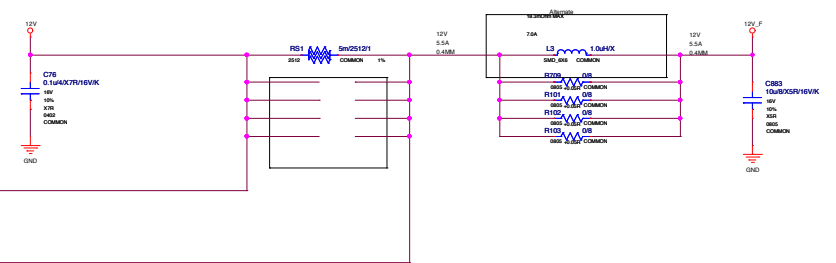




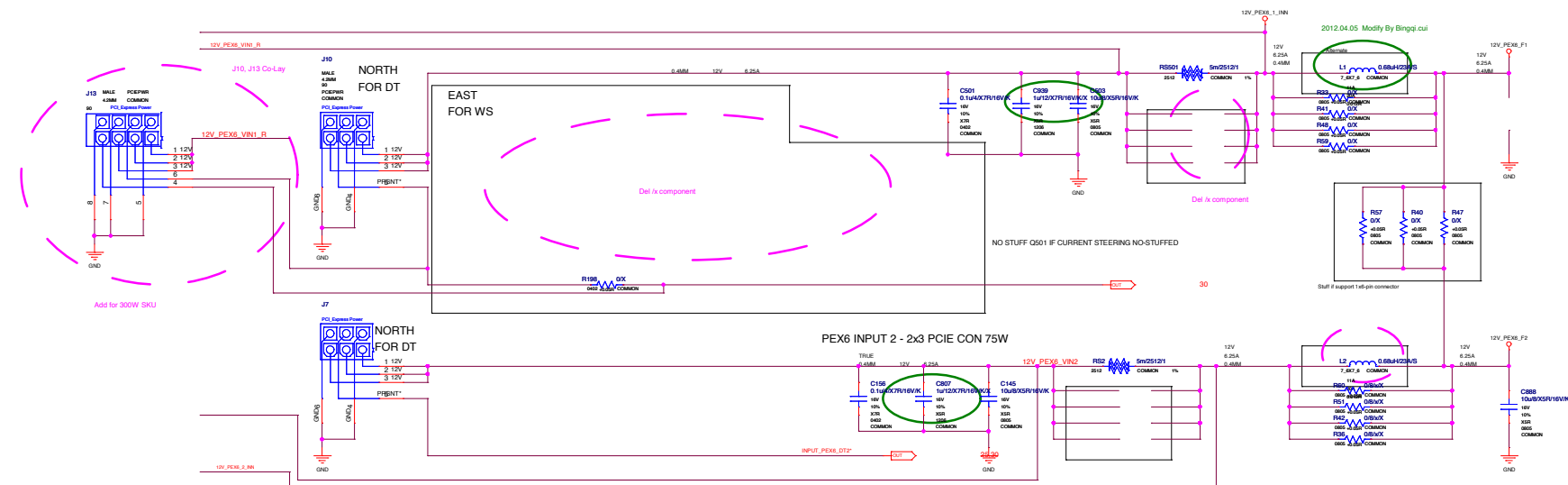
Page 20: I/O inputs, filtering, and monitoring



PEX_12V INPUT - 66W



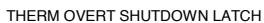
PEX6 INPUT 1 - 2x3 PCIE CON 75W



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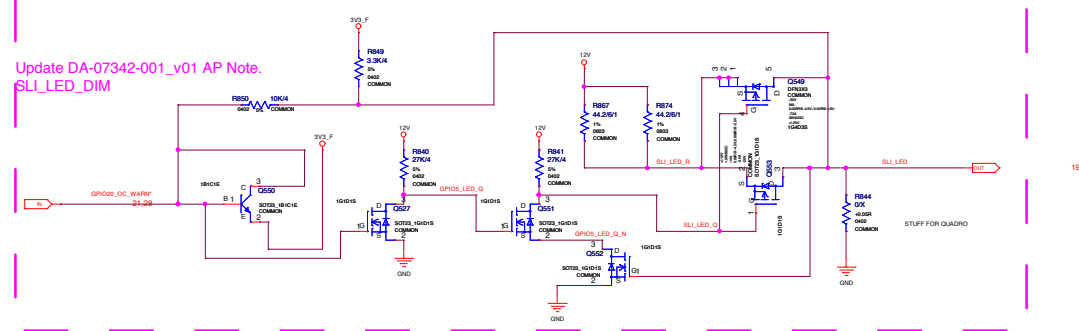
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PS: Inputs, Filtering, and Monitoring



IFP_IOVDD (backdrive prevention)

Del old SLI LED

Update DA-07342-001_v01 AP Note.
SLI_LED_DIM

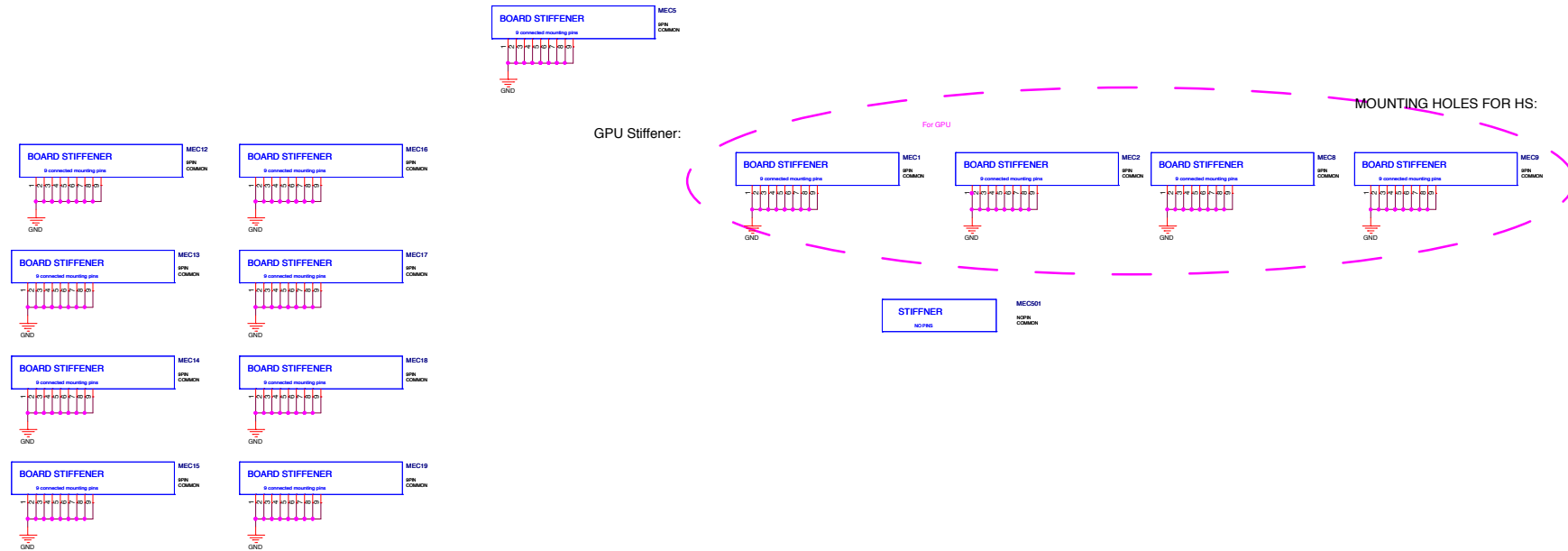


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Brackets:

MH-C236D118-8

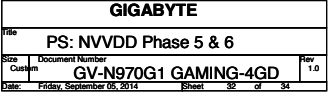


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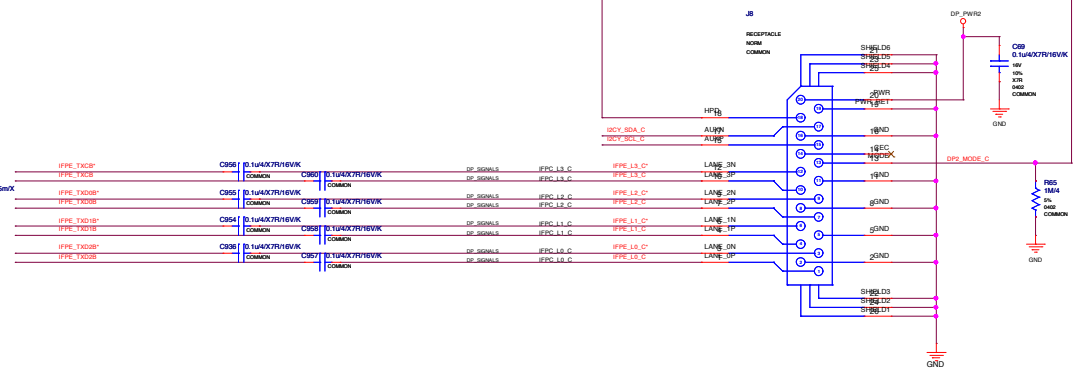
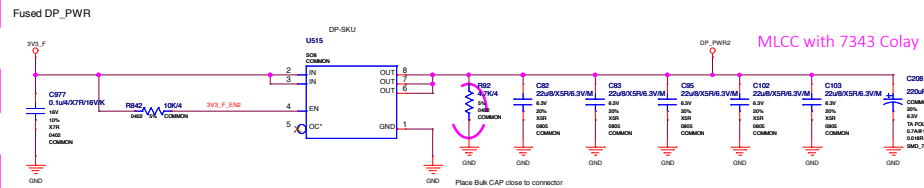
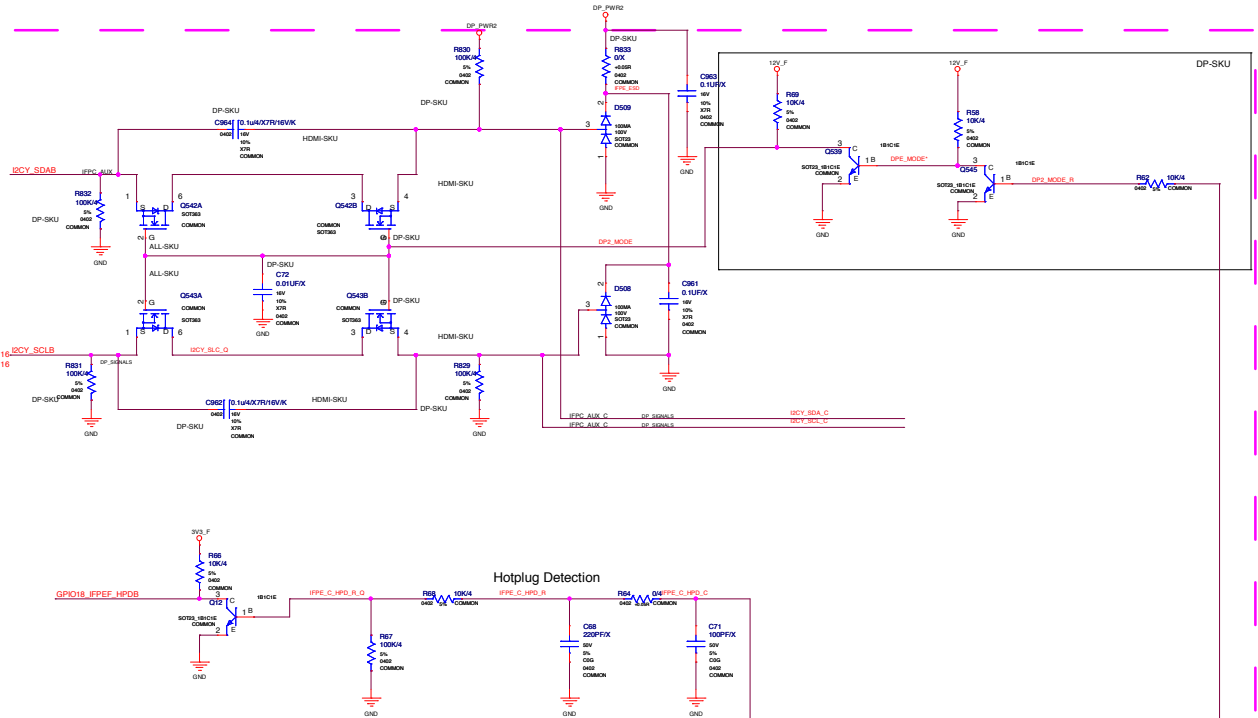
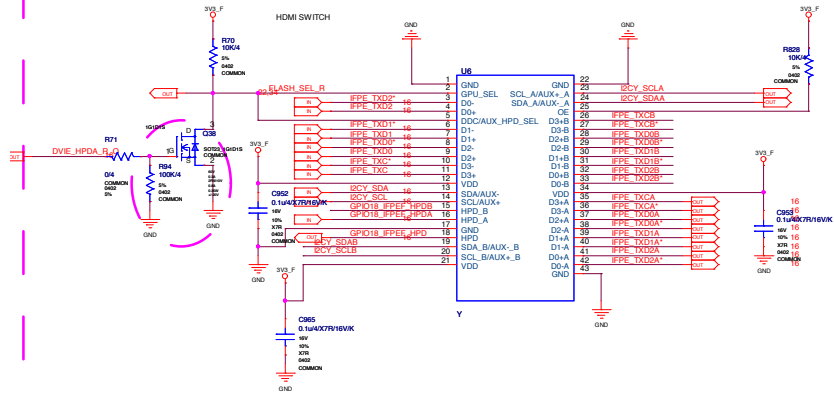
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Two cases to be considered:

- 1.THE SEL TO HIGHT,CONNECT DVI WORK
2. THE SEL TO LOW, CONNECT HDMI WORK



1. THE SEL TO HIGHT, CONNECT DVI WORK
2. THE SEL TO LOW, CONNECT HDMI WORK

